

CONTROLLED RGBW LED

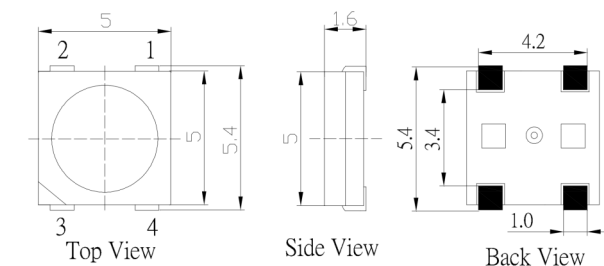
P/N: SCA-7650-S000



■Features

- Top SMD internal integrated high quality external control line serial cascade constant current IC.
- control circuit and the chip in SMD 5050 components, to form a complete control of pixel, color mixing uniformity and consistency
- built-in data shaping circuit, a pixel signal is received after wave shaping and output waveform distortion will not guarantee a line;
- The built-in power on reset and reset circuit, the power does not work;
- gray level adjusting circuit (256 level gray scale adjustable)
- red drive special treatment, color balance
- line data transmission;
- plastic forward strengthening technology, the transmission distance between two points over 10M Using a typical data transmission frequency of 800 Kbps, when the refresh rate of 30 frames per sec

■Outline Dimension



NO.	Symbol	Function description
1	VDD	Power supply LED
2	DOUT	Control data signal output
3	VSS	Ground
4	DIN	Control data signal input

Unit:mm
Tolerance:±0.20mm
unless otherwise noted

■Applications

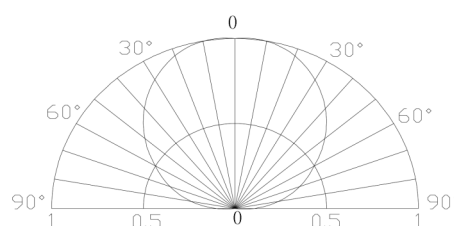
- Full color LED string light, LED full color module, LED super hard and soft lights, LED guardrail tube, LED appearance / scene lighting
- LED point light, LED pixel screen, LED shaped screen, a variety of electronic products, electrical equipment etc..

■Absolute Maximum Rating

(Ta=25°C VSS=0V)

Item	Symbol	Value	Unit
Power supply voltage	V _{DD}	+3.5~+5.5	V
Logic input voltage	V _{IN}	-0.5~V _{DD} +0.5	V
Working temperature	T _{opt}	-40~+85	°C
Storage Temperature	T _{stg}	-40 ~ +105	°C
ESD pressure	V _{ESD}	4K	V
Lead Soldering Temperature	T _{sol}	260°C/10sec	-

■Directivity



■LED characteristic parameter

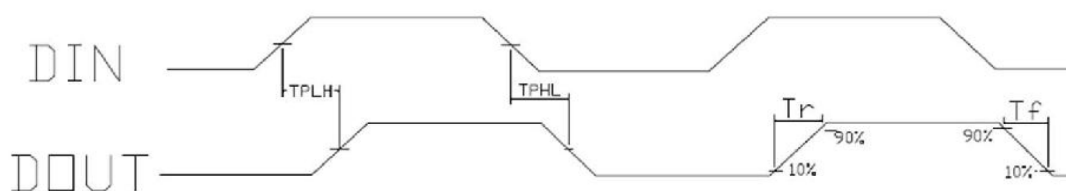
Emitting color	Wavelength (nm)/CCT(K)
Red	620-630
Green	515-525
Blue	460-470
White	6000-7000K

■The electrical parameters (unless otherwise specified, TA=-20 ~ +70 °C, VDD=4.5 ~ 5.5V, VSS=0V):

Parmeter	Symbol	Min	Typical	Max	Unit	Test conditions
The chip supply voltage	VDD	---	5.2	---	V	---
R/G/B port pressure	VDS,MAX	---	---	26	V	---
DOUT drive capability	IDOH	---	49	---	mA	DOUT conect ground, the maximum drive current
	IDOL	---	-50	---	mA	DOUT conect +, the largest current
The signal input flip threshold	VIH	3.4	---	---	V	VDD=5.0V
	VIL	---	---	1.6	V	
The frequency of PWM	FPWM	---	1.2	---	KHZ	---
Static power consumption	IDD	---	1	---	mA	---

■The dynamic parameters (Ta=25 °C):

Parameter	Symbol	Min	Typical	Max	Unit	Test conditions
The speed of data transmission	fDIN	---	800	---	KHZ	The duty ratio of 67% (data 1)
DOUT transmission delay	TPLH	---	---	500	ns	DIN→DOUT
	TPHL	---	---	500	ns	
IOUT Rise/Drop Time	Tr	---	---	40	ns	VDS=1.5 IOUT=9mA
	Tf	---	---	80	ns	



■The data transmission time (TH+TL=1.25μs±600ns):

T0H	0 code, high level time	0.3μs	±0.15μs
T0L	0 code, low level time	0.9μs	±0.15μs
T1H	1 code, high level time	0.6μs	±0.15μs
T1L	1 code, low level time	0.6μs	±0.15μs
Trst	Reset code, low level time	80μs	